

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of PCT/FR03/00092
Docket No: Q82454

Jean-Pierre BOUZIDI

Appln. No.: Not Assigned

Confirmation No.: Not Assigned

Group Art Unit: Not Assigned

Filed: July 23, 2004

Examiner: Not Assigned

For: DIFFERENTIAL INPUT STAGE FOR ELECTRONIC EQUIPMENT, COMPRISING
MEANS FOR REDUCING INTERFERENCE CAUSED BY A VOLTAGE OR
CURRENT IN COMMON MODE

PRELIMINARY AMENDMENT**MAIL STOP AMENDMENT**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the above-identified application as follows on the
accompanying pages.

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